09/980754 C13 Rec'd PCT/PTO 02 NOV 2001

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

"Express Mail" Mailing Label Number <u>EL 436 586 985 US</u> Date of Deposit <u>November 2, 2001</u>

I hereby certify that this paper and/or fee is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to the Commissioner of Patents and Trademarks, Washington, D.C. 20231.

180b 5-1910

In the Application of Wilfred Lerch, et al.

Ser. No.:

Not Yet Known (Based on PCT/EP00/03664 filed April 22, 2000 and German

priority documents 199 20 322.9 filed May 3, 1999 and 19927 962.4 filed

June 18, 1999)

For:

METHOD OF GENERATING DEFECTS IN A GRID STRUCTURE OF A

MICONDUCTOR MATERIAL

Filed on:

November 3, 2001

Assistant Commissioner for Patents

Washington, DC 20231

PRELIMINARY AMENDMENT ACCOMPANYING PCT NATIONAL STAGE APPLICATION

Sir:

Prior to examination, please amend the above-identified application as follows.

IN THE SPECIFICATION:

On page 1, immediately after the title, please insert the following heading:

--Background of the Invention--.

On page 3, line 1, please insert the following heading:

--Summary of the Invention--.

On page 8, between lines 10 and 11, please insert the following heading:

--Brief Description of the Drawing--;

On page 9, between lines 17 and 18, please insert the following heading:

--Description of Preferred Embodiments--.

On page 10, line 9, after the word "torr", please insert -- "(133 X 10⁻⁶ Pa)"--.

- 1 -